



Lee 19-6-4

UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Lee et al.  
Case: 19-6-4  
Serial No.: 09/785,604  
Filing Date: February 16, 2001  
Group: 2116  
Examiner: Thuan N. Du

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: Gene Maurer Date: November 8, 2005

Title: Method and Apparatus for Distributing a Self-Synchronized Clock to Nodes on a Chip

TRANSMITTAL OF APPEAL BRIEF

Mail Stop Appeal Brief  
Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Appeal Brief; and
2. Copy of Notice of Appeal, filed on September 6, 2005, with copy of stamped return postcard indicating receipt of Notice by PTO on September 8, 2005.

There is an additional fee of \$500 due in conjunction with this submission under 37 CFR §1.17(c). Please charge **Deposit Account No. 50-0762** the amount of \$500, to cover this fee. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully,

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Receipt in the USPTO is hereby acknowledged of:

Transmittal Letter – (Original & 1 copy)  
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Case Name: Lee 19-6-4  
Serial No.: 09/785,604

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